FNmega128 / FN-2400 Errata

FN-ERR-2400 Rev. 7 2015-02

Documented Errata

The following behaviours are known and documented for the current firmware (2.6.12) and hardware revision (3.2).

ERR-001 — Configuration CRC not recalculated on direct writes

The configuration record carries a CRC16 field. The ordinary configuration interface recalculates this CRC when it updates the record. Direct memory writes (POKE) to the configuration record do not trigger CRC recalculation, so the stored CRC may not match the record contents after service memory writes. The control core does not reject records on CRC mismatch.

ERR-002 — Thermal data path accepts the full FN-TEMP8 range

Setpoints enforced by the configuration interface are limited to the operating-mode ranges. The underlying thermal data path accepts any valid FN-TEMP8 value. Setpoints written directly to the configuration record are therefore not clamped to the operating-mode range, and the control core will drive the chamber toward such a setpoint.

ERR-003 — Mirror registers overwritten each control cycle

The setpoint and mode mirror registers (0x0034, 0x0035) and the sensor registers (0x0030–0x0033) are refreshed by the control core every control cycle. Values written directly to these registers are overwritten on the next cycle.

ERR-004 — Negative-zero FN-TEMP8 normalized

FN-TEMP8 value 0x80 (negative zero) is accepted on input and treated as 0 °C. It is normalized to 0x00 when written by the firmware.

ERR-005 — History ring buffer does not clear stale entries

The chamber-temperature history ring buffer advances a head index and wraps at its capacity without clearing previously written entries. Stale samples beyond the current head may remain visible in memory.

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